Amendments to the Specification:

Please insert the following paragraph on page 1, before "Field of the Invention".

[0000.1]

This application claims the benefit of International Patent Application No. PCT/JP2004/017573, filed November 26, 2004, which claims priority of Japanese Patent Application No. 2003-409204, filed December 8, 2003.

Please amend the following paragraph as indicated.

[0030]

According to the present invention, the first feedback loop includes: a selection circuit supplied with a reference clock signal and with ~~said received clock signal~~ said received data signal to output one of the signals based on a selection control signal; a voltage-controlled oscillator circuit for varying the oscillation frequency based on an input control signal voltage; a first phase detector circuit supplied with a clock signal output from said voltage-controlled oscillator circuit and with a signal from said selection circuit to detect the phase difference therebetween; and a first integrator circuit for integrating an output of said first phase detector circuit to supply the resulting output voltage as a control signal voltage to said voltage-controlled oscillator circuit, while the second feedback loop includes: a discriminator circuit supplied with said received data signal; a second phase detector circuit supplied with an output of said discriminator circuit and with said received data signal to detect the phase difference therebetween; a second integrator circuit for integrating an output of said second phase detector circuit; and a phase shift circuit supplied with a clock signal output from said voltage-controlled oscillator circuit and with an integrated output of said second integrator circuit to shift the phase of the input clock signal in accordance with said integrated output supplied to output the resulting clock signal; the clock signal output from said phase shift circuit being supplied to said discriminator circuit as a signal for discrimination and being output as an output clock signal.